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Fig. 1

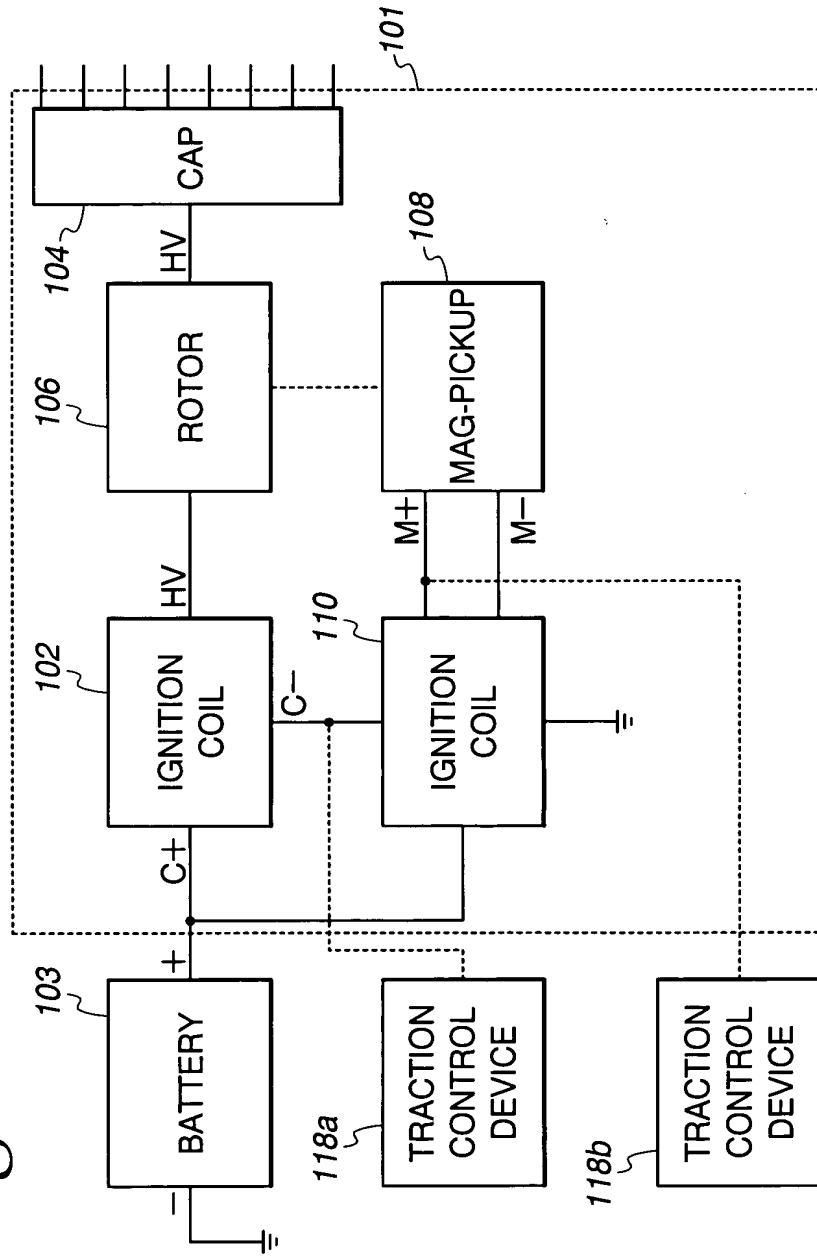
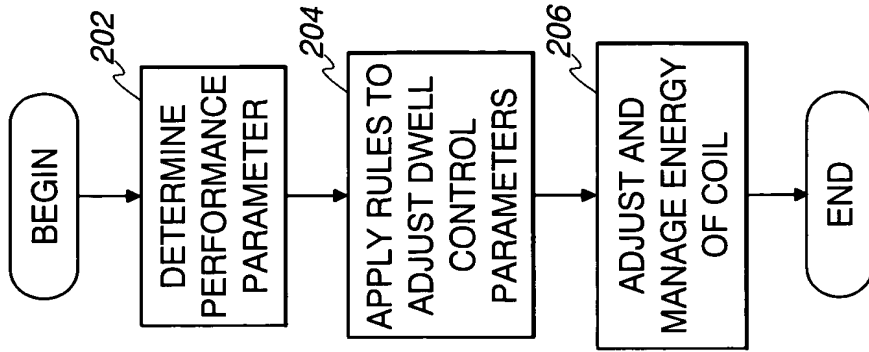
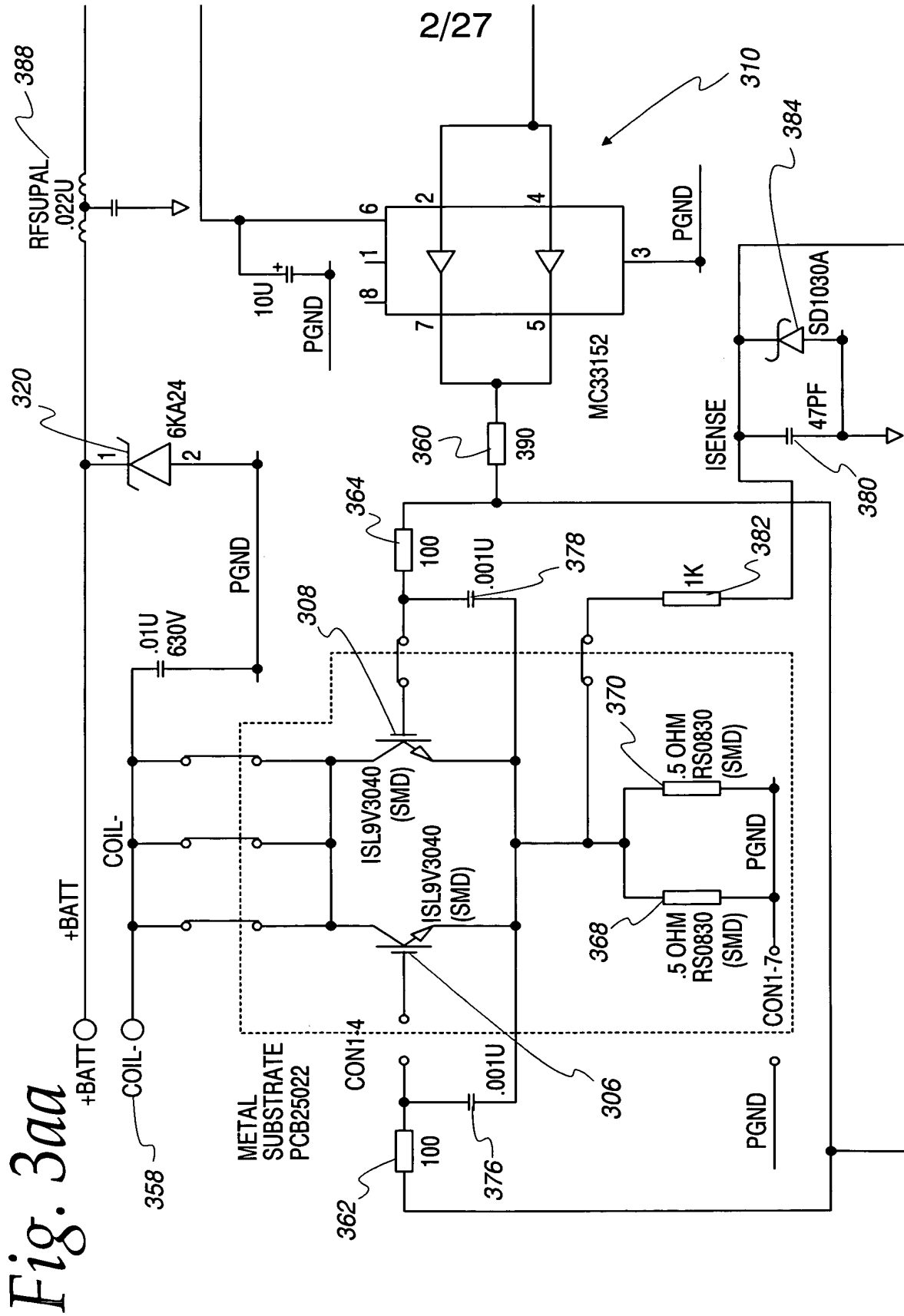


Fig. 2







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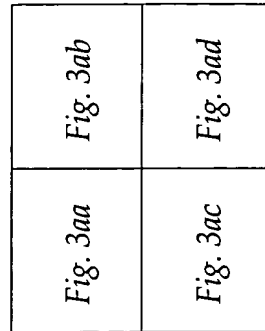
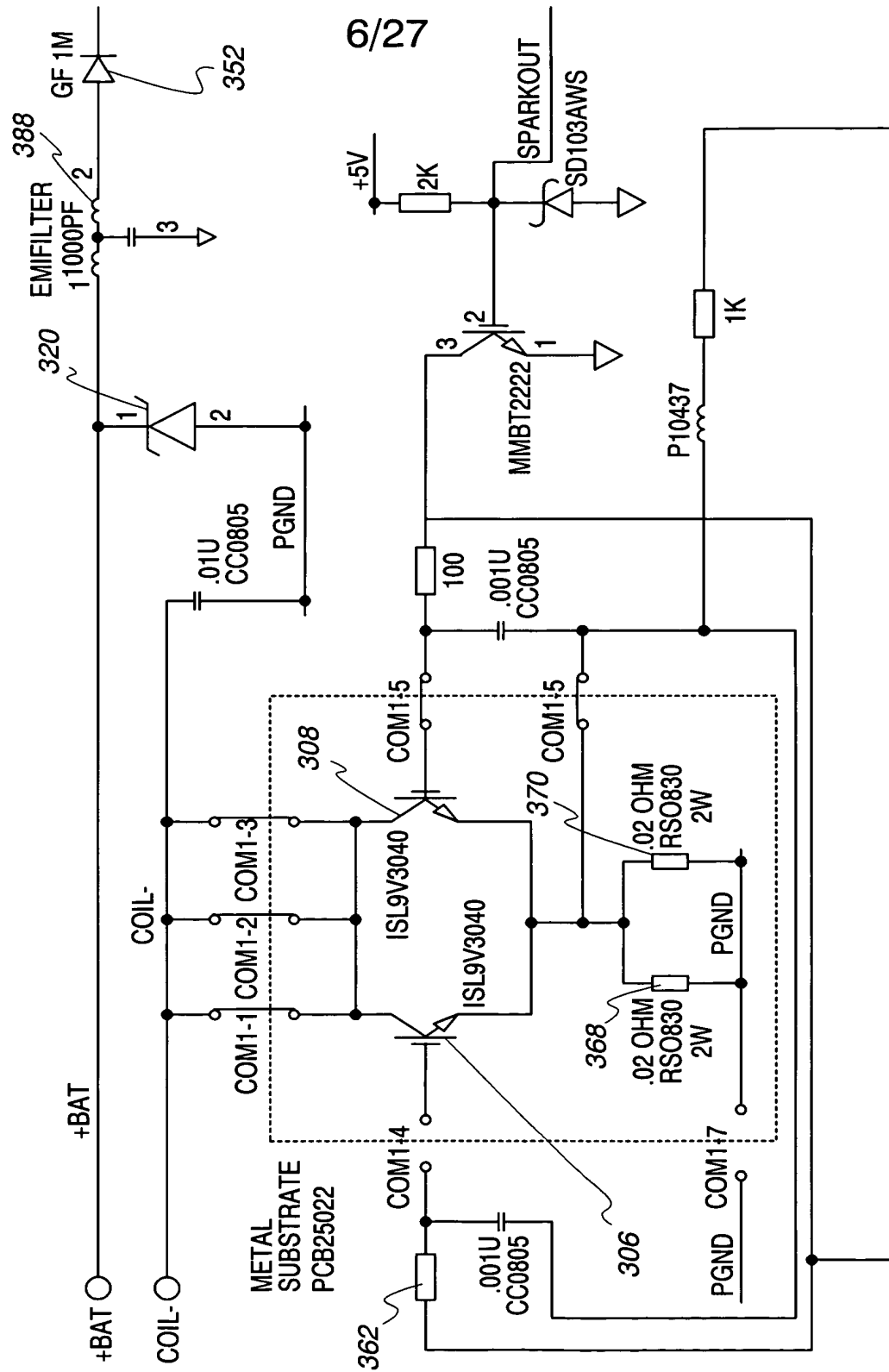


Fig. 3ac

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The schematic diagram illustrates the power supply and signal conditioning circuit for the 316 module. The power supply section features a +7.5V input connected to a 240 OHM RS2010 1/2W 5% resistor, followed by a ZM4738AD1 Zener diode (cathode to input, anode to ground). The output of the Zener diode is connected to the IN pin of a MIC2920A POSITIVE VOLTAGE REGULATOR. The regulator's OUT pin is connected to a +5V output and a .1U capacitor to ground. The signal conditioning section includes a 10K resistor connected to the FB1 input of the regulator. The FB1 input is also connected to a 560 1W RS2512 resistor, which is in parallel with another 560 1W RS2512 resistor. The output of the FB1 input is connected to the GREEN TACH OUT (OPTIONAL) signal. The signal is also connected to a P10437 FERRITE bead and a .1U capacitor to ground. The signal is then connected to the CON1 connector, which is labeled SMD-CON7. The connector pins are numbered 1 through 7, with 1 being the signal pin, 2 being ground, and 3 through 7 being other pins. The signal is also connected to the FB1 input of the regulator.

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Fig. 3bd

Fig. 3bc

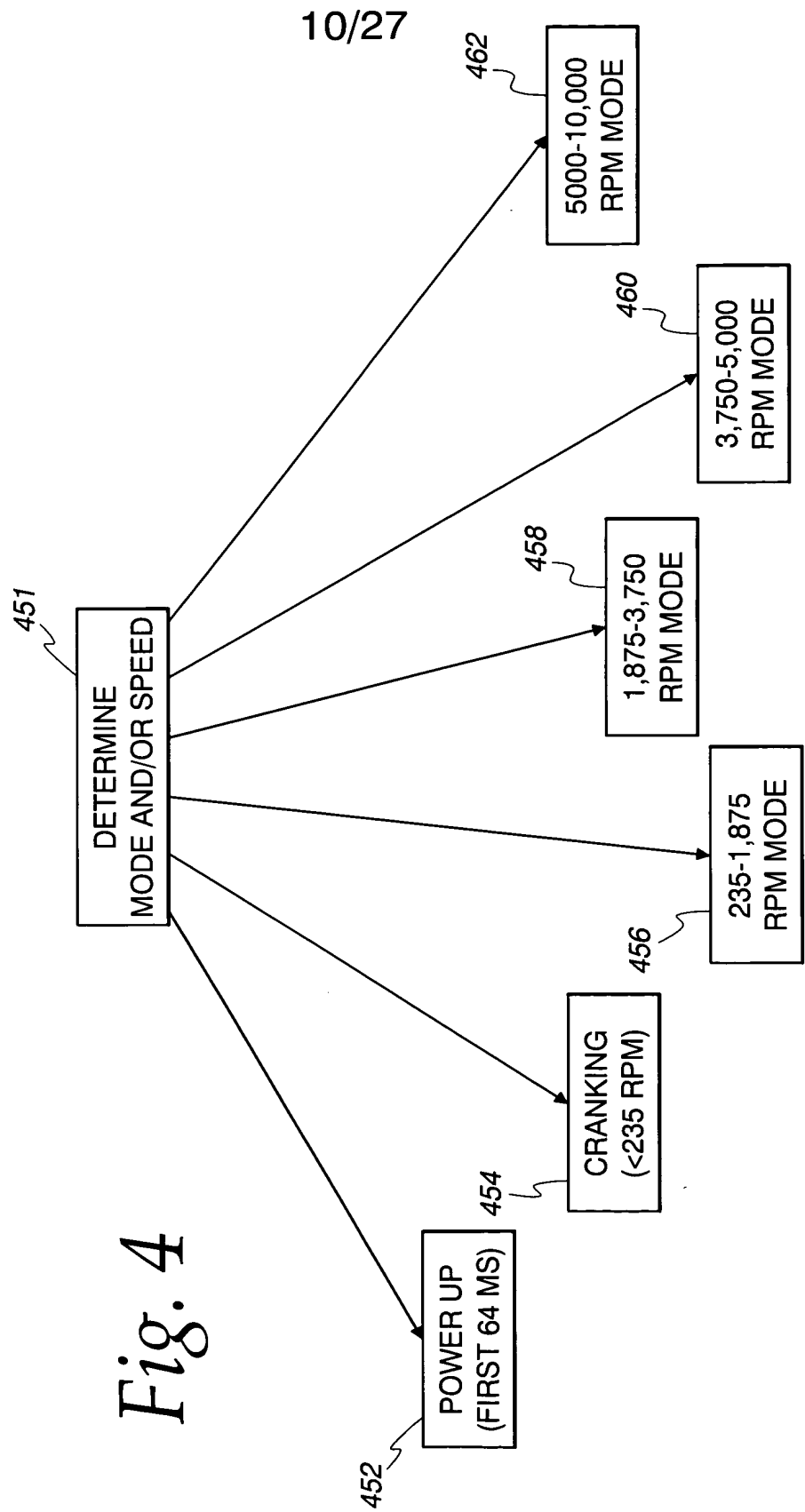


Fig. 4

# Fig. 5A 11/27

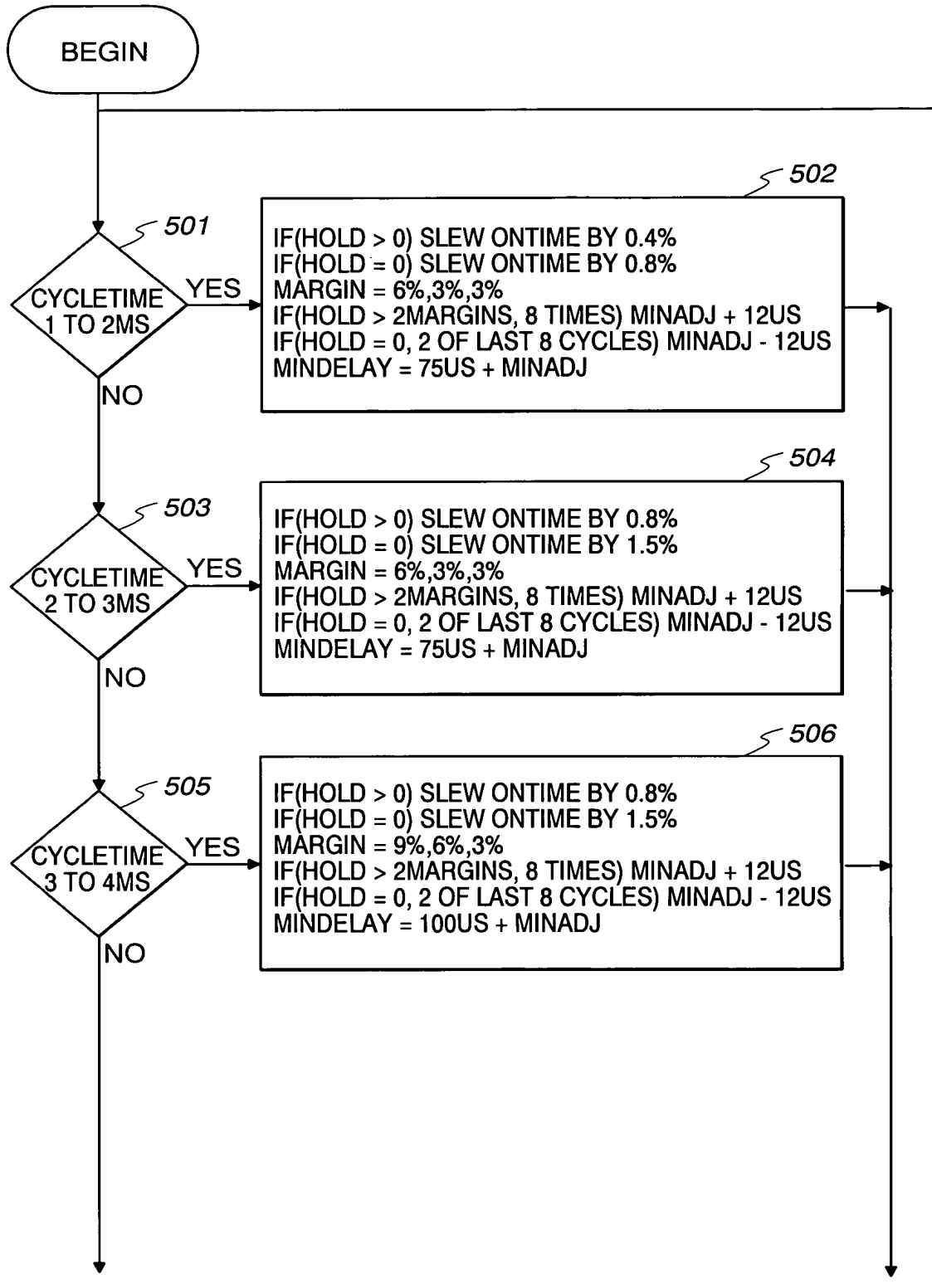
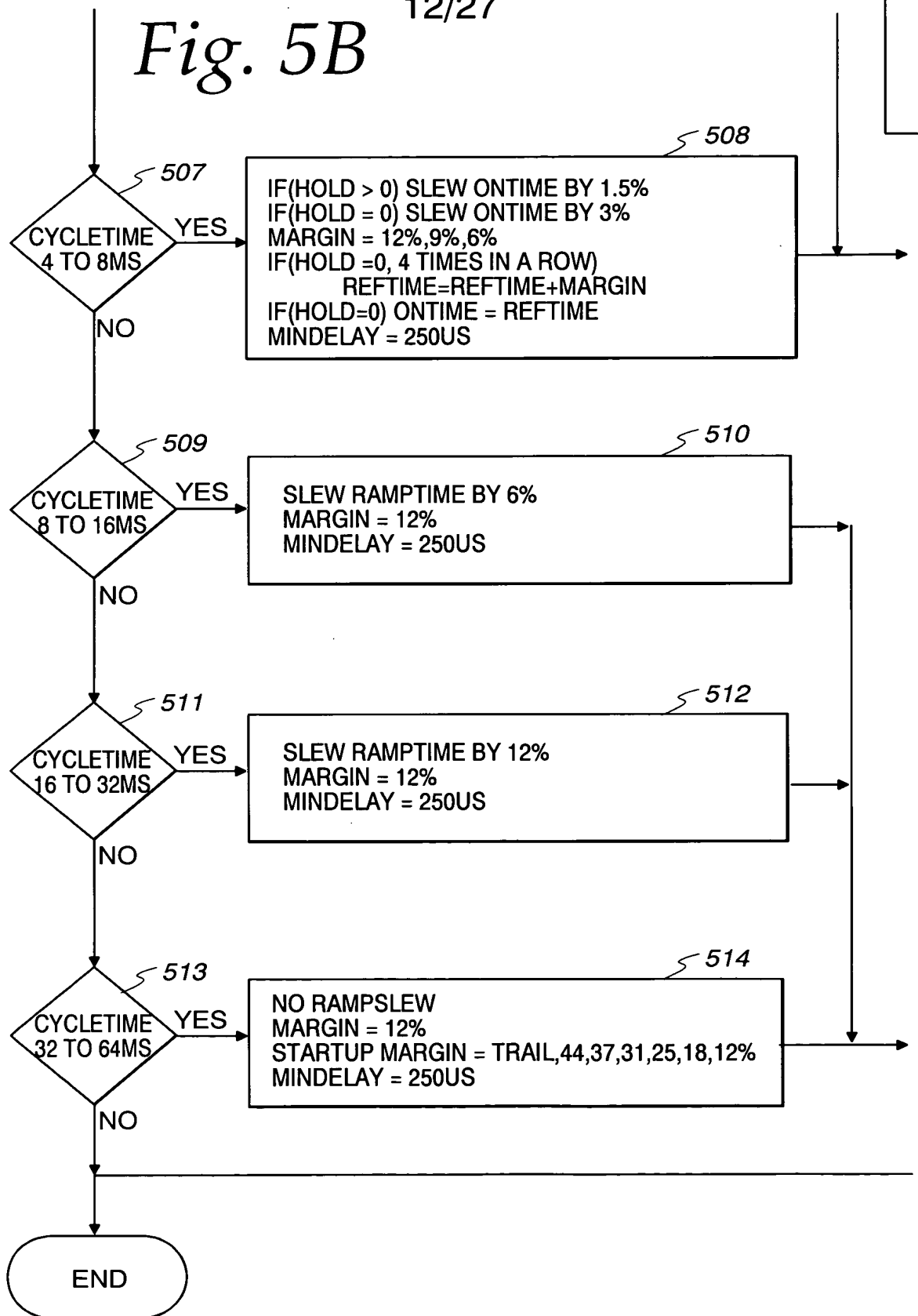


Fig. 5B<sup>12/27</sup>



*Fig. 5C*

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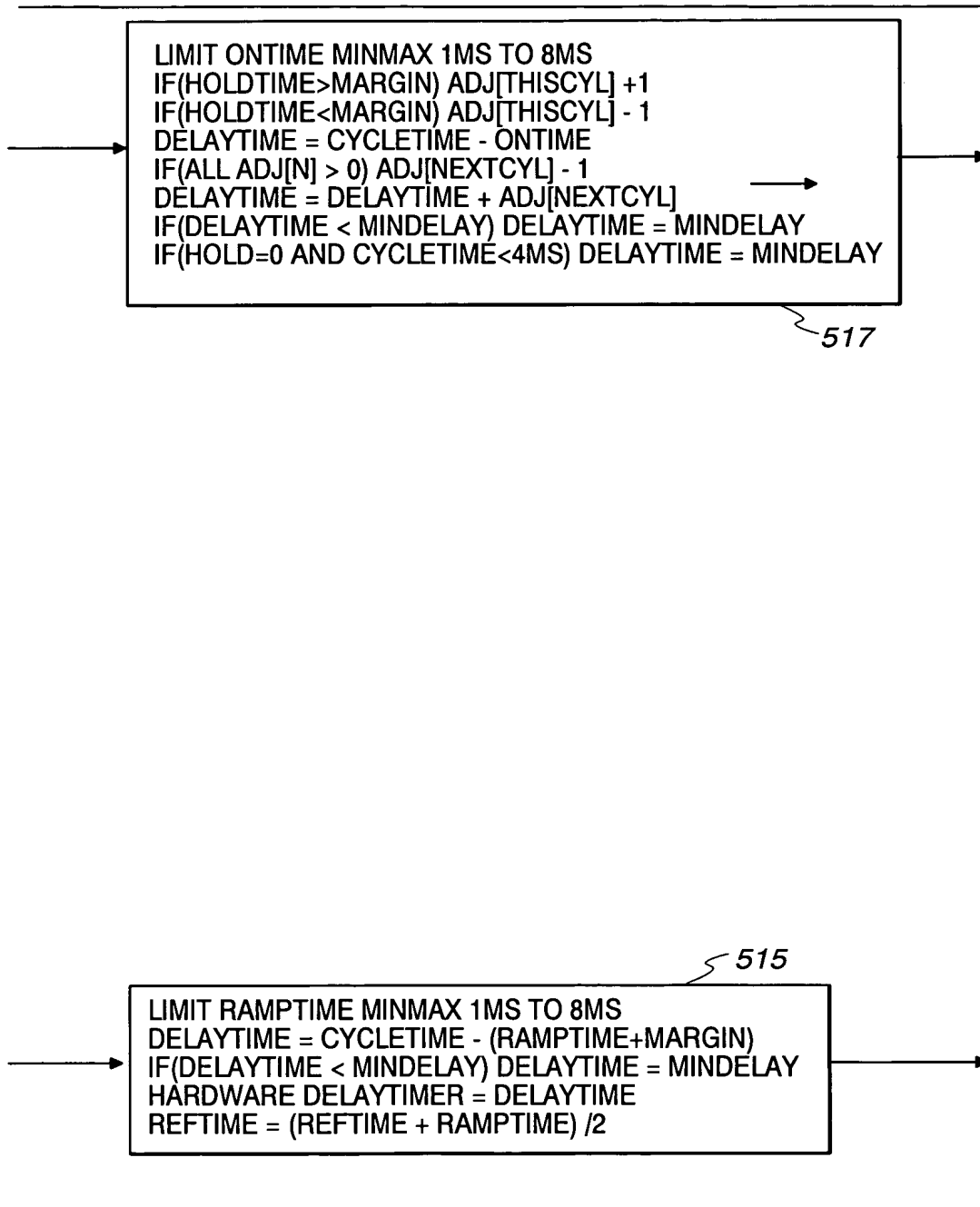
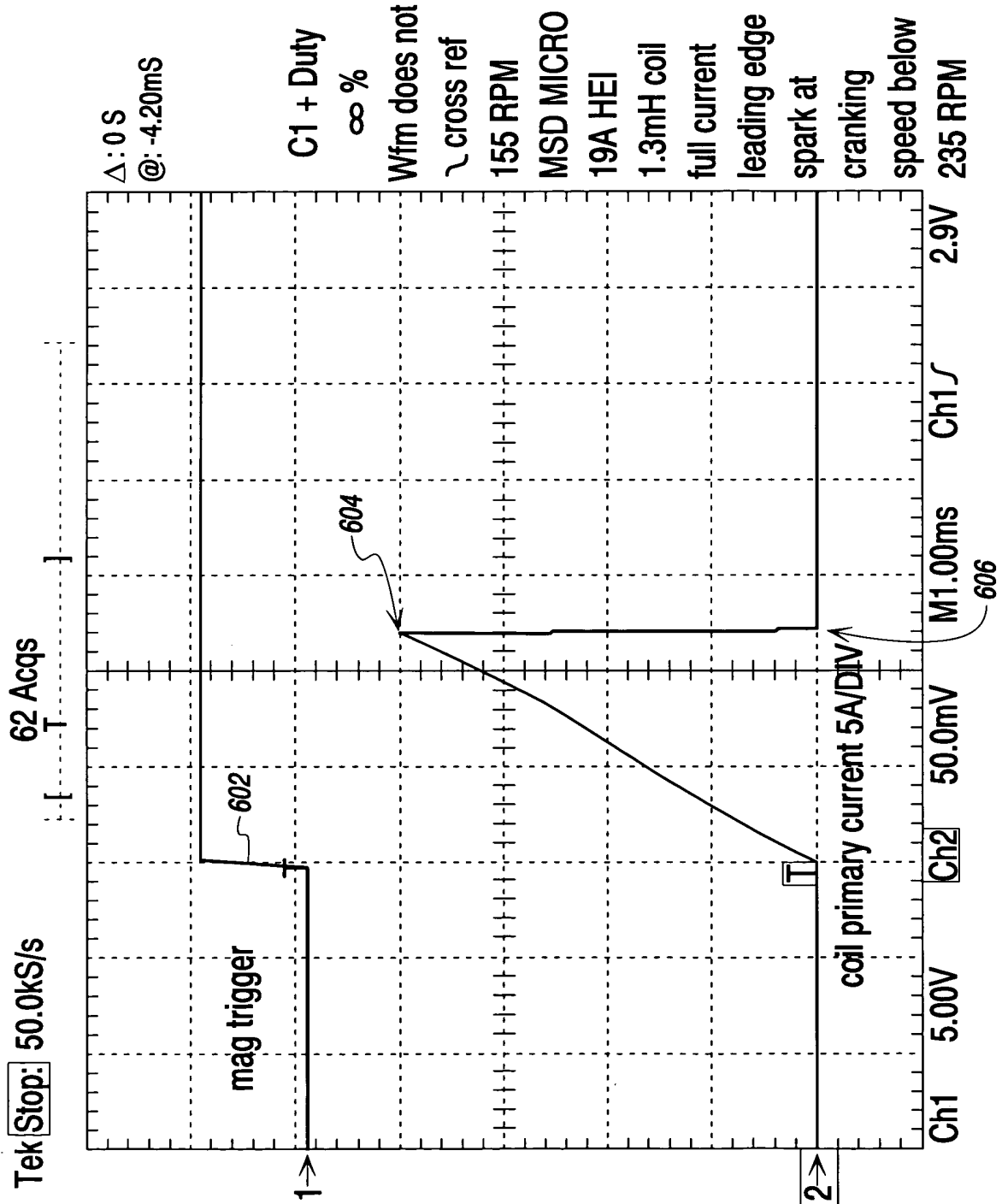


Fig. 5A

Fig. 5B Fig. 5C

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Fig. 6



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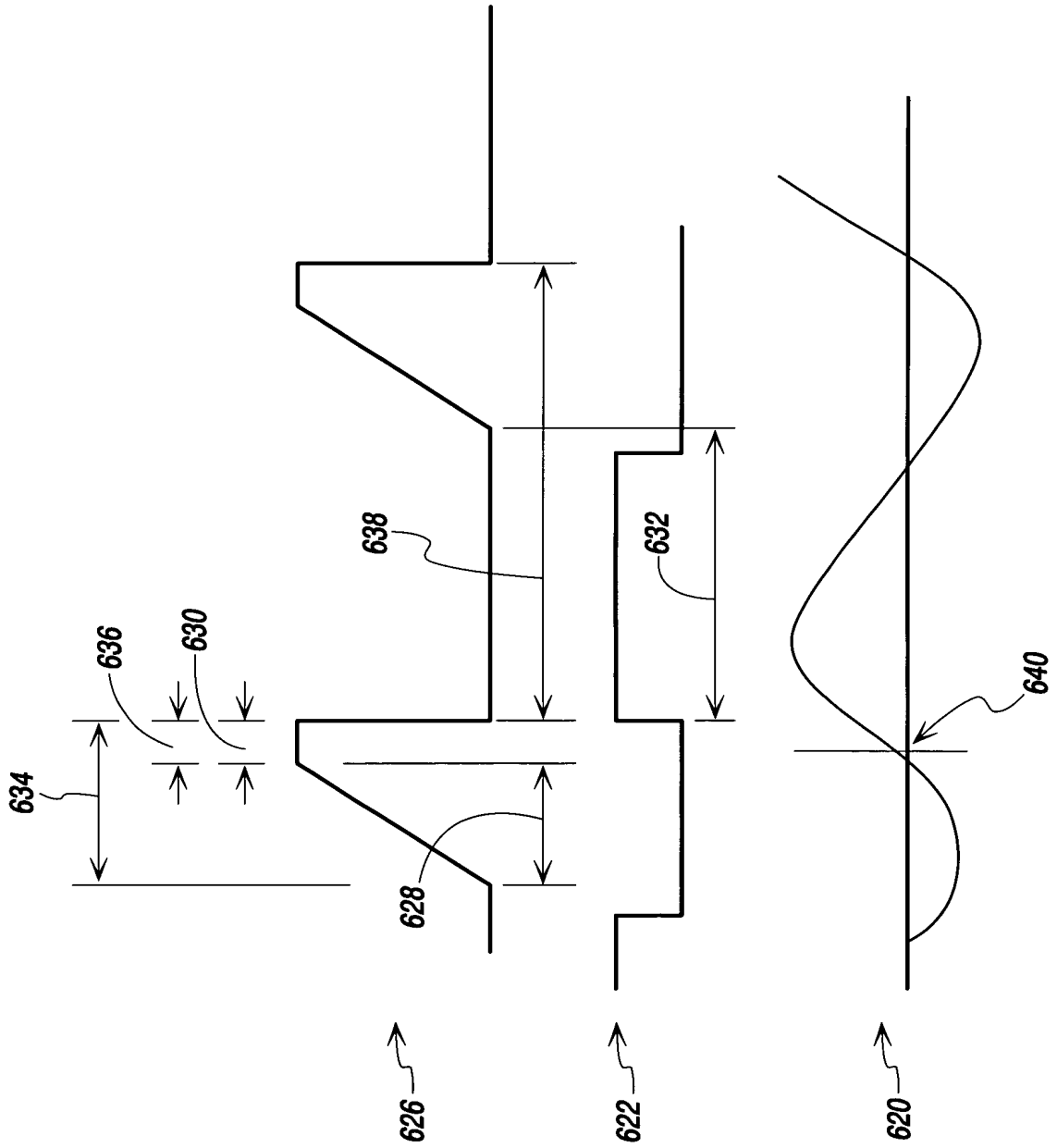


Fig. 7

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Fig. 9

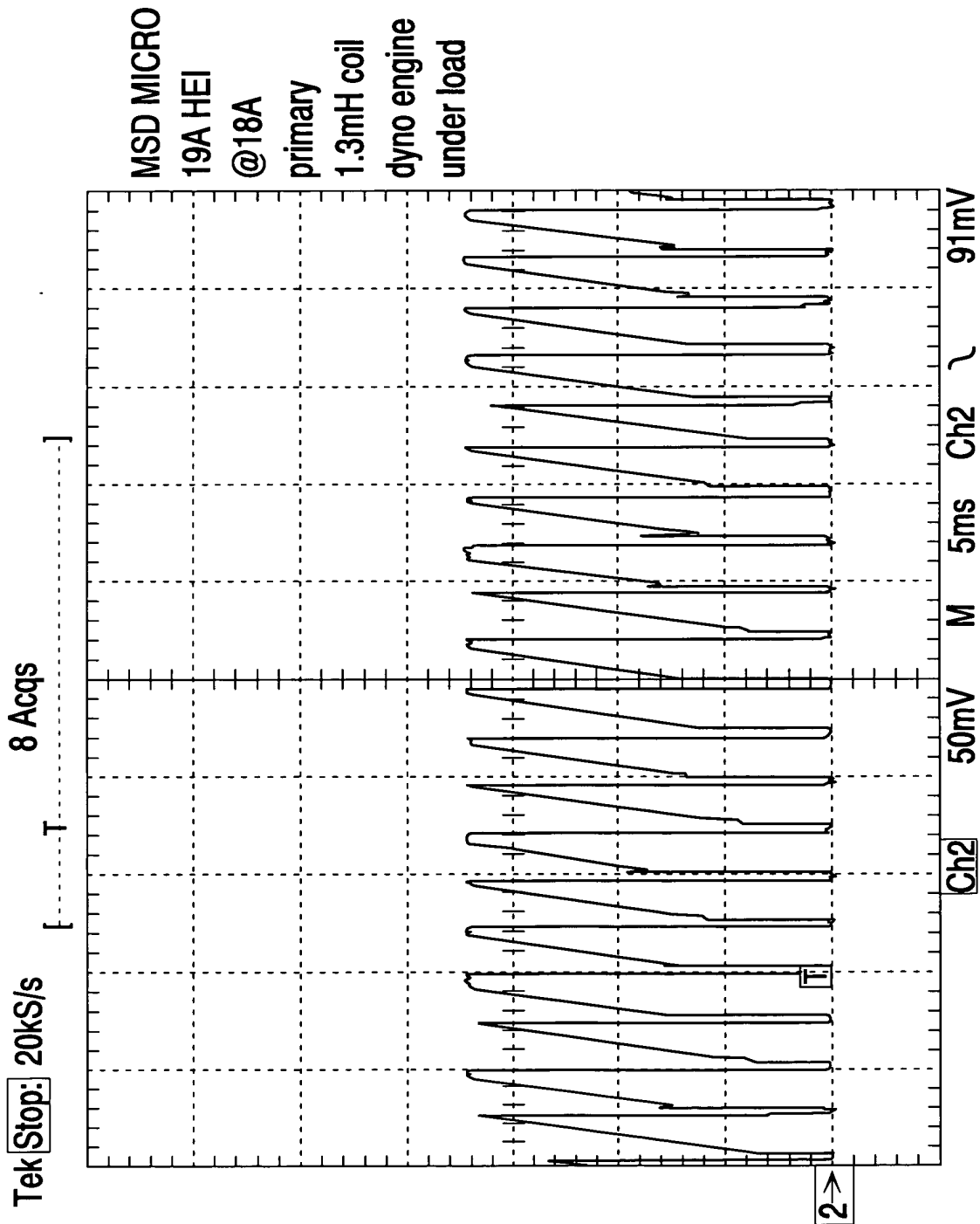
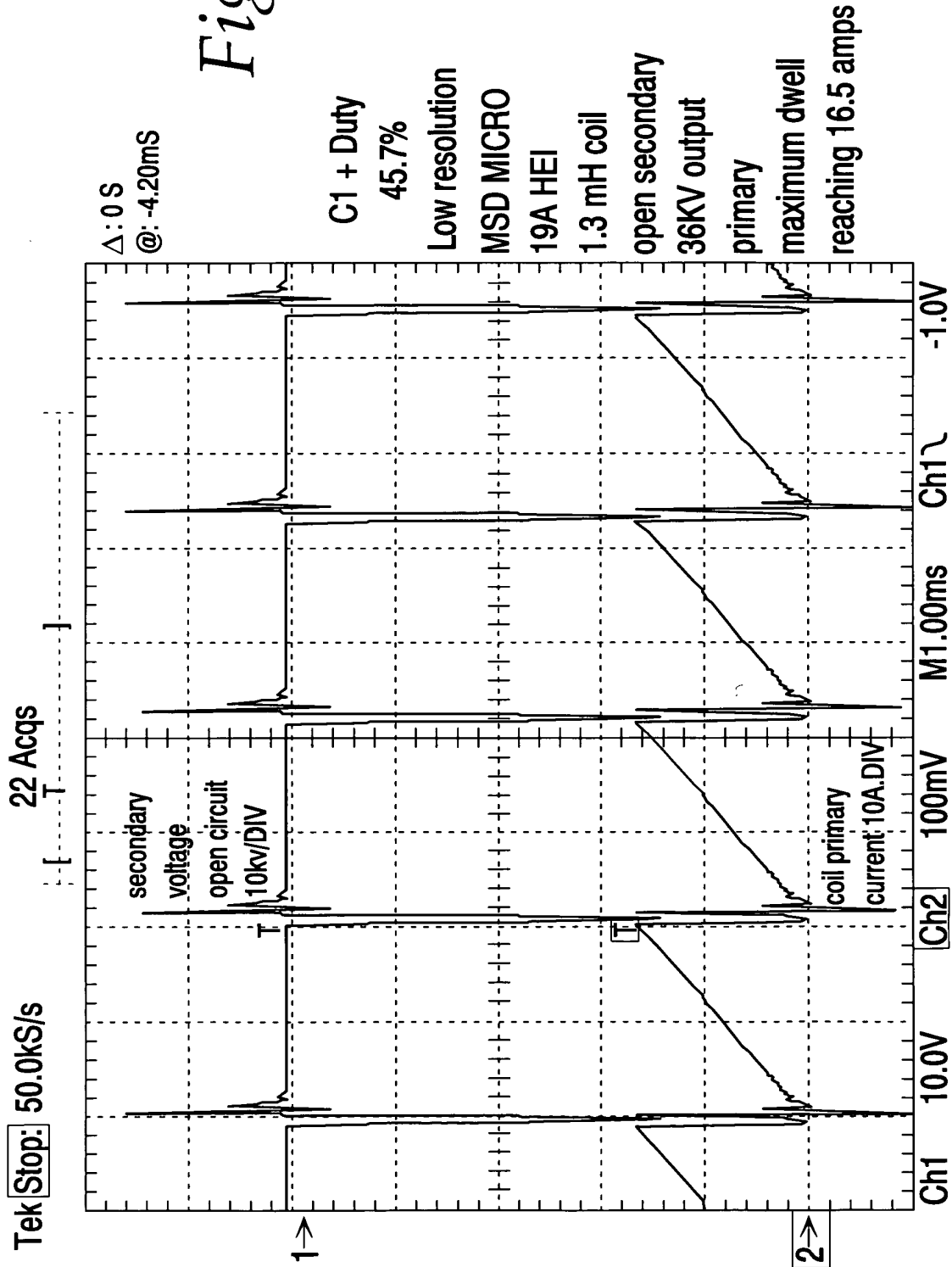


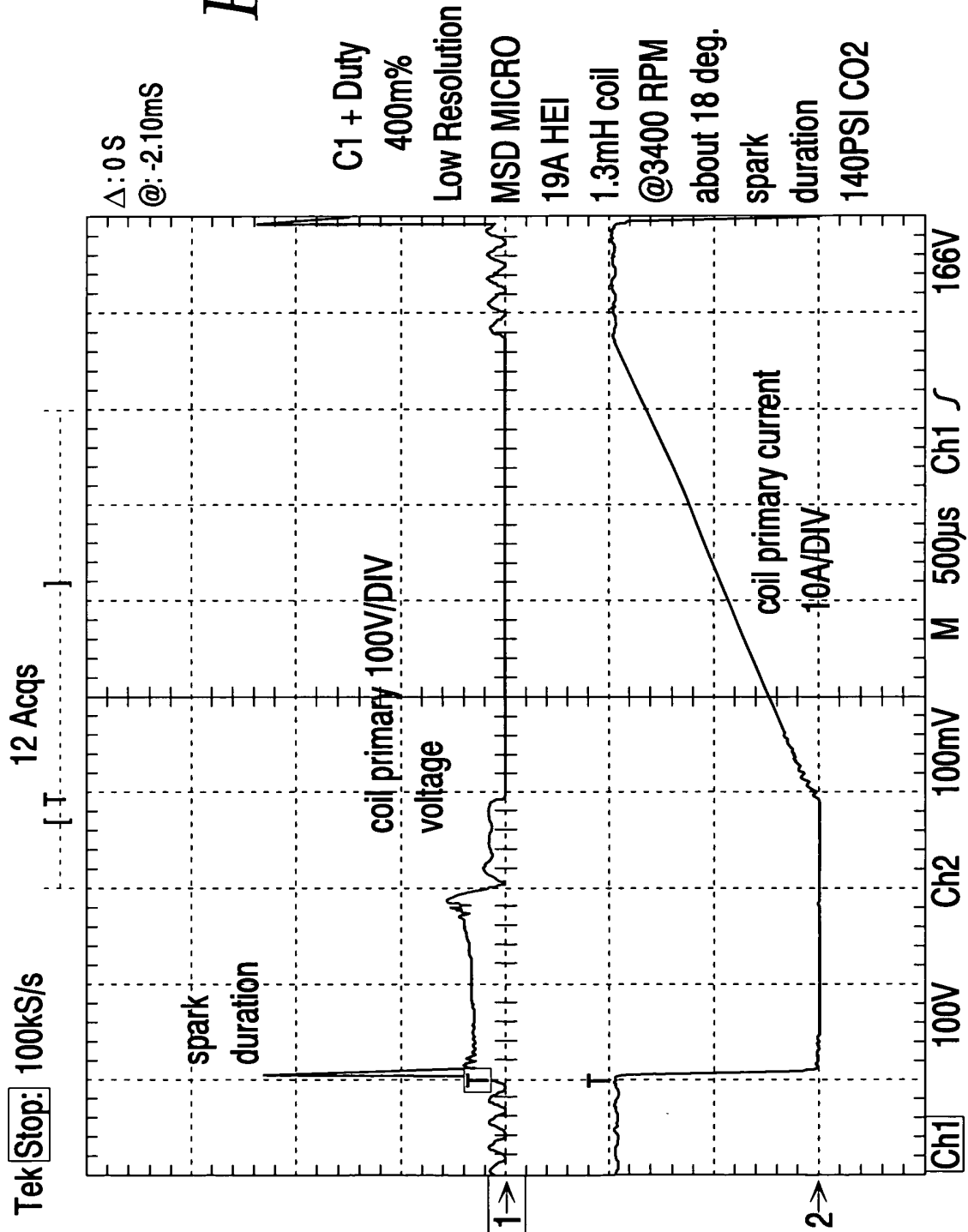
Fig. 10

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Fig. 11



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Fig. 12

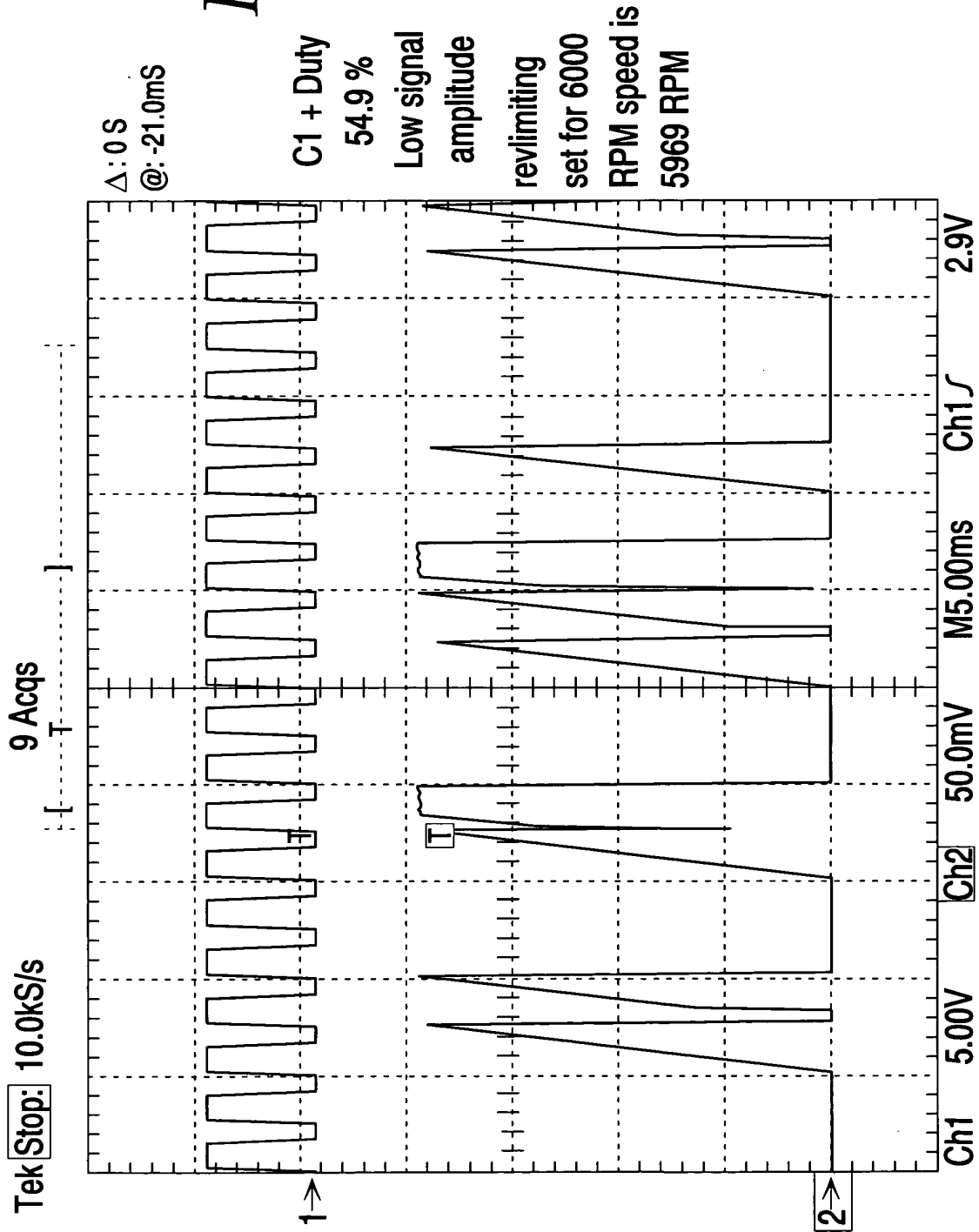


Fig. 13

## C1 + Duty

54.8%

**Low signal  
amplitude**

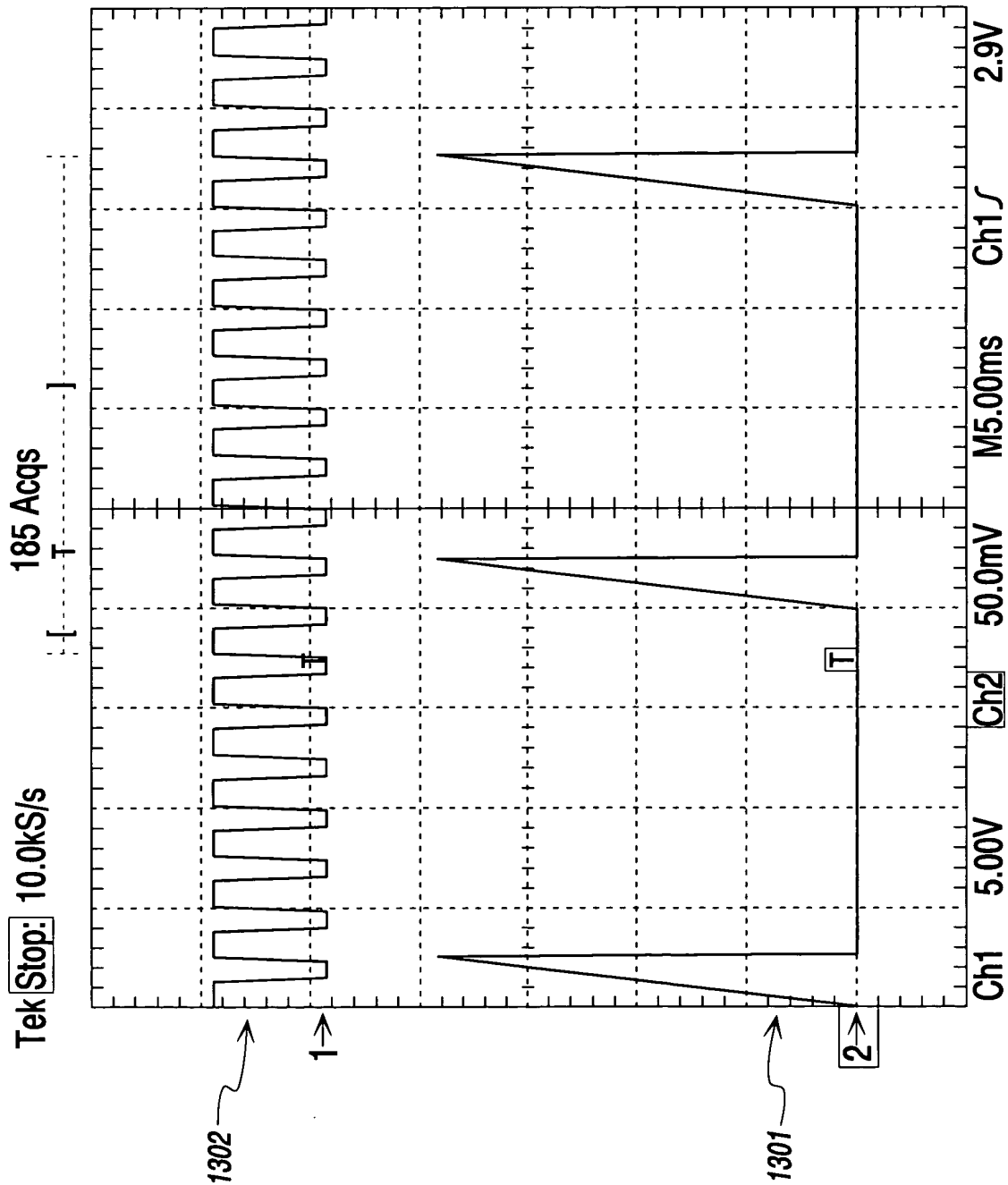
# revlimiting

**revlimiter**

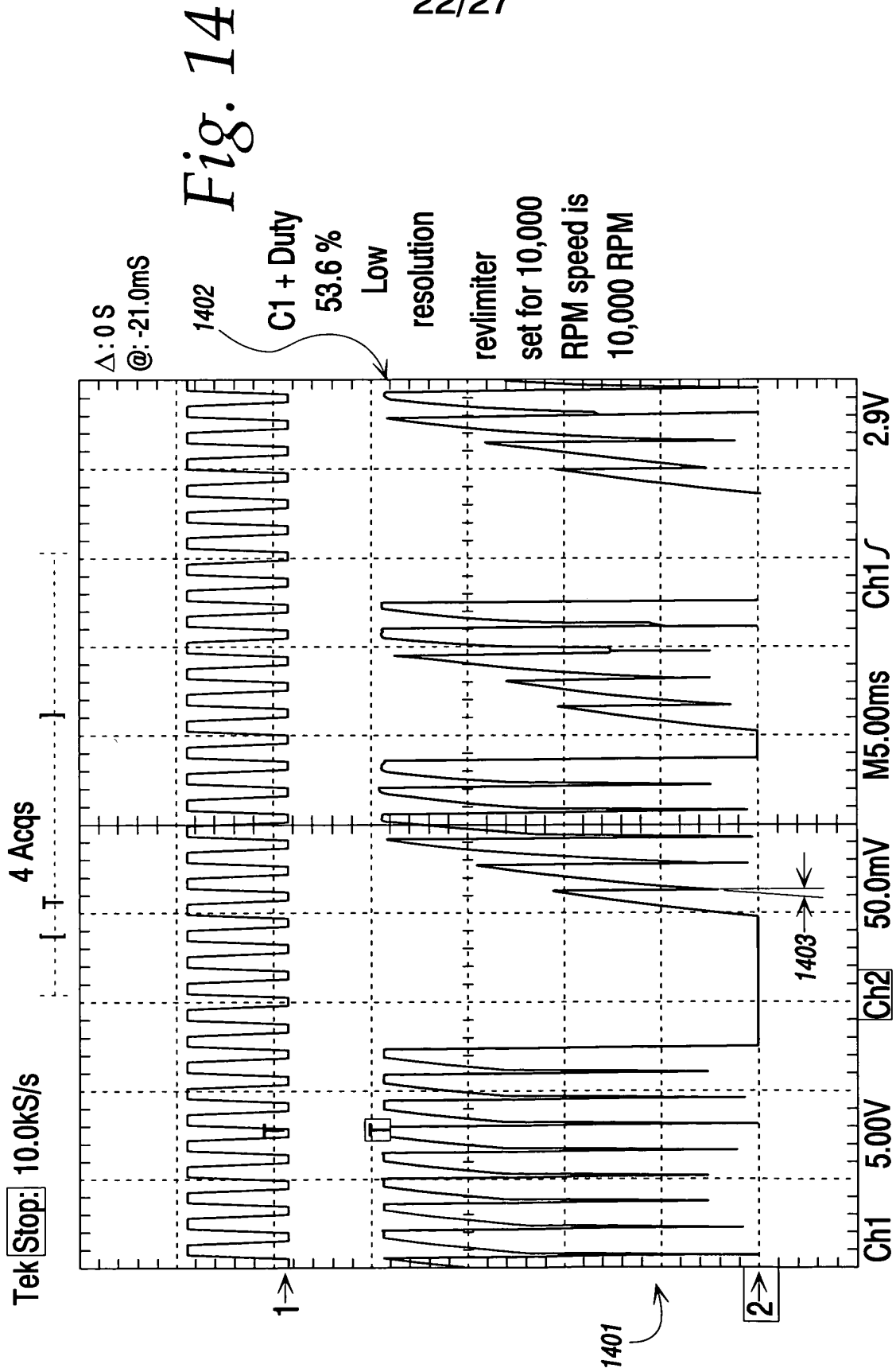
**set for 6000**

## RPM speed is

6000 RPM



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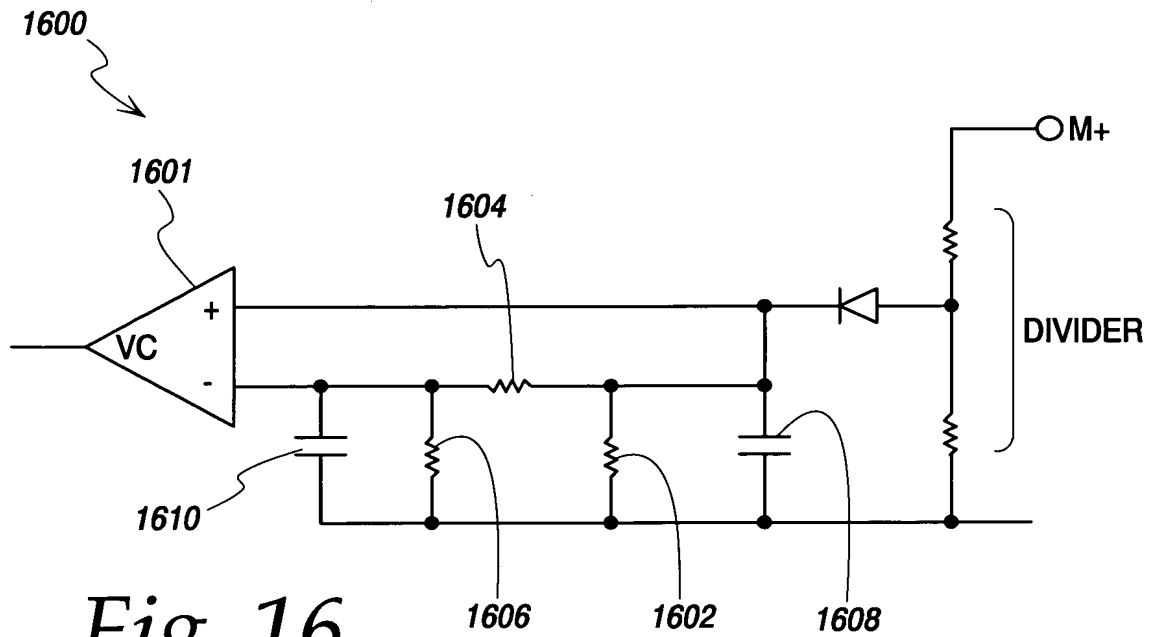
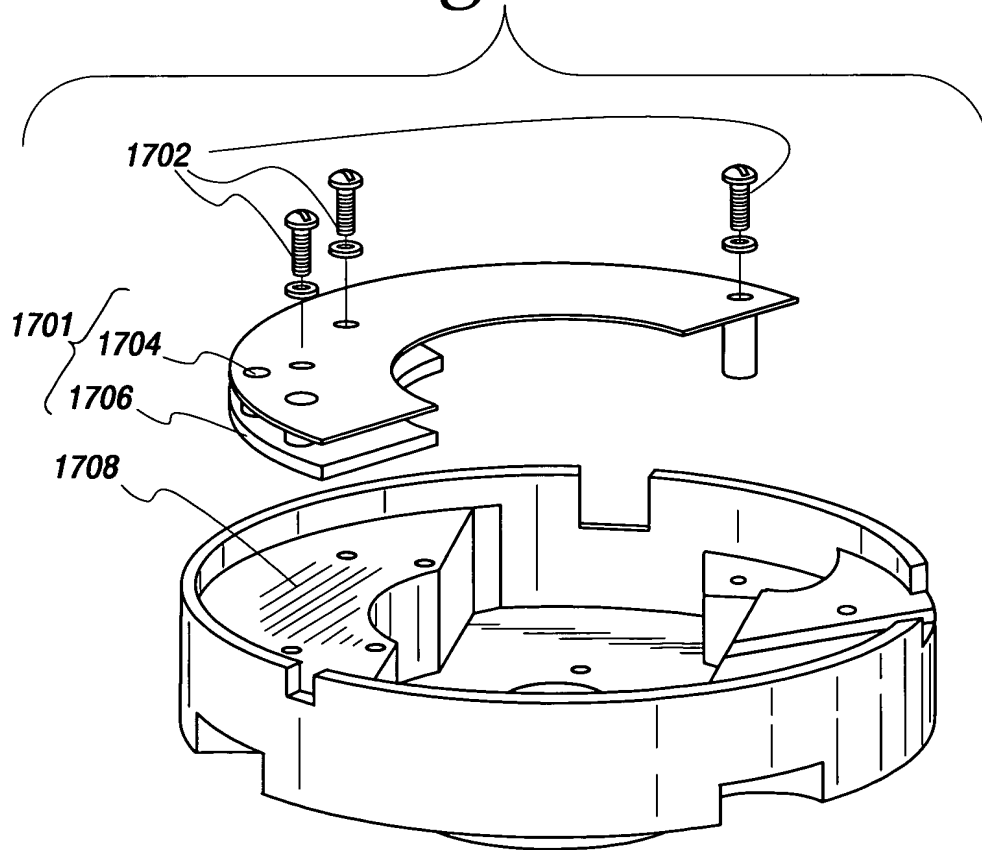


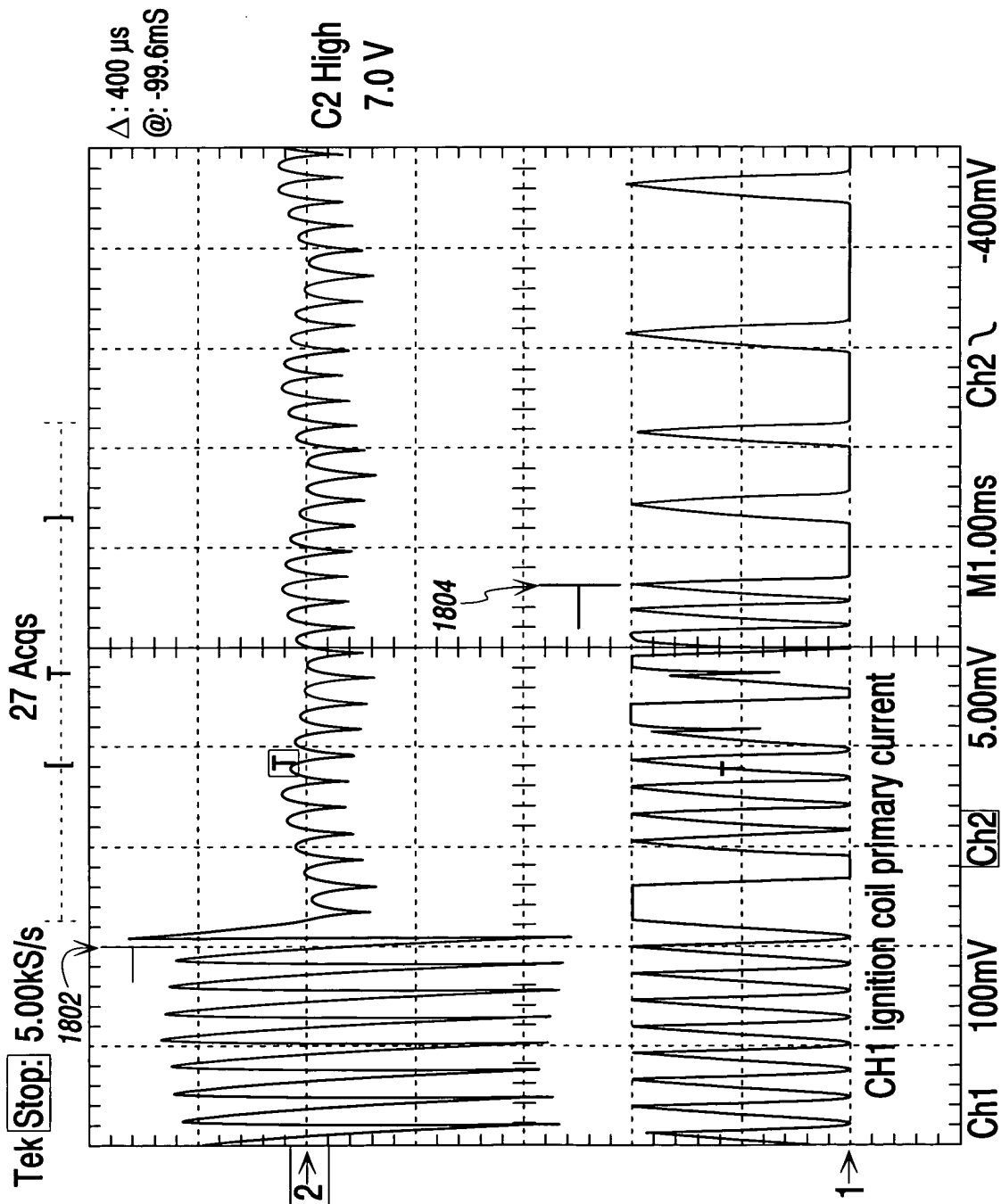
Fig. 16

Fig. 17





**$\Delta$ : 400  $\mu$ s**  
**@: -99.6mS**



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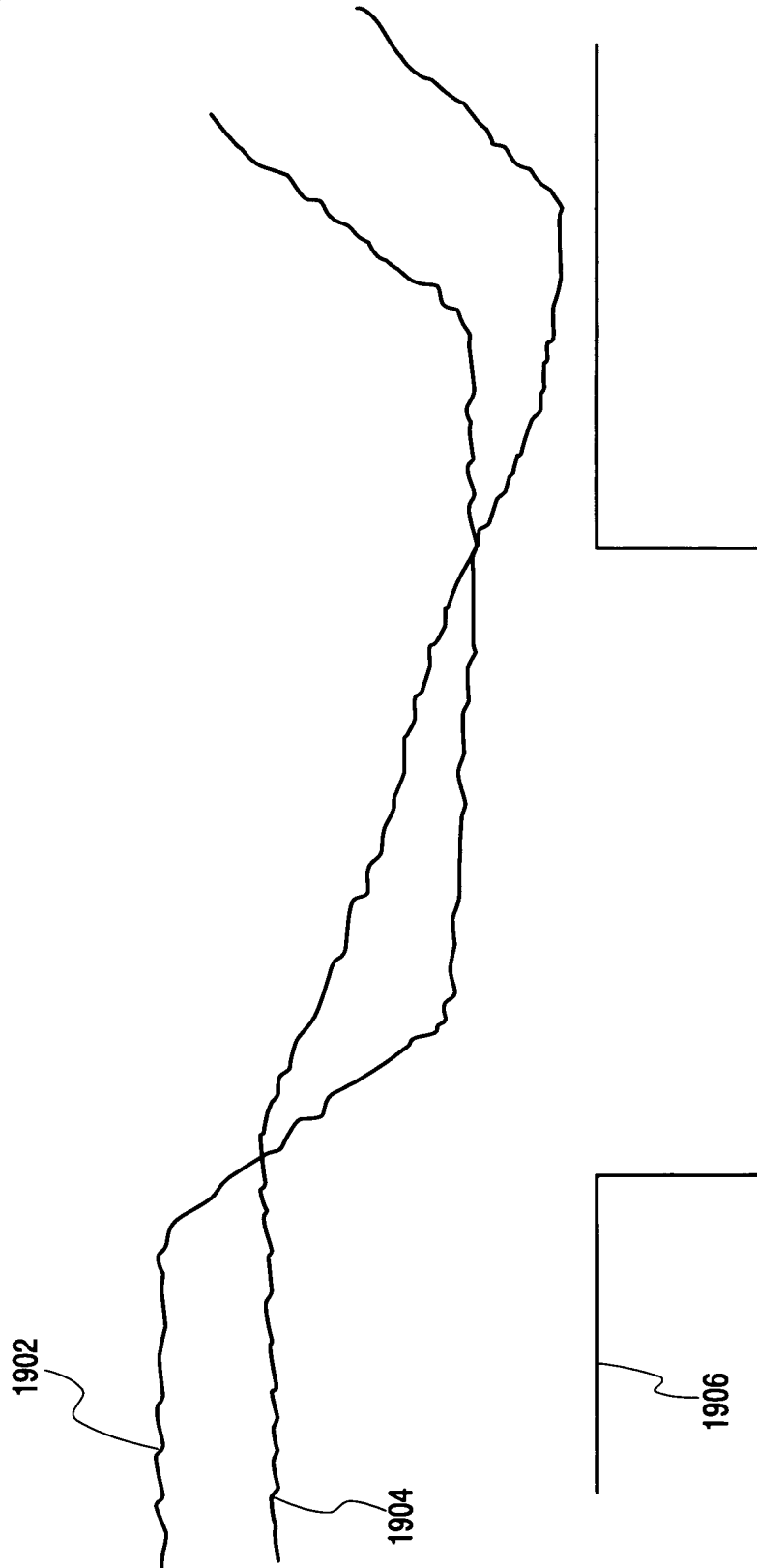


Fig. 19

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Fig. 20

